

**Amendments to the Specification**

**On page 1, line 3, please amend the title as follows:**

**INTERCHANGEABLE CONNECTION ARRAYS FOR  
DOUBLE-SIDED ~~DIMM~~ MEMORY MODULE PLACEMENT**

**On page 1, line 24, please replace the paragraph as follows:**

~~Figures 3a and 3b show alternative arrangements of memory  
packages on a substrate.~~

Figure 3 shows an alternative arrangement of memory packages on  
a substrate.